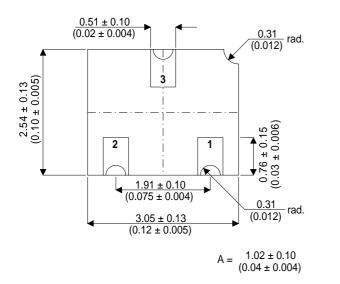
SMLA42CSM



MECHANICAL DATA Dimensions in mm (inches)

SILICON NPN HIGH VOLTAGE TRANSISTOR IN CERAMIC SURFACE MOUNT PACKAGE

С



SOT23 CERAMIC (LCC1 PACKAGE)

Underside View

PAD 1 – Base PAD 2 – Emitter PAD 3 – Collector

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V _{CBO}	Collector – Base Voltage	300V		
V _{CEO}	Collector – Emitter Voltage	300V		
V _{EBO}	Emitter – Base Voltage	6V		
I _C	Collector Current	500mA		
P _D	Total Device Dissipation	350mW		
	Derate Above 25°C	2.0mW/°C		
т _ј	Maximum Junction Temperature	200°C		
T _{stg}	Storage Temperature Range	–55 to 200°C		

А

1.40

(0.055)

max.

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

E

F

FEATURES

- HIGH BREAKDOWN VOLTAGE
- LOW SATURATION VOLTAGES
- LOW CAPACITANCE
- HERMETIC CERAMIC SURFACE MOUNT PACKAGE SOT23CSM (SOT23 COMPATIBLE)



SMLA42CSM

ELECTRICAL CHARACTERISTICS (T_{case} = 25°C unless otherwise stated)

Parameter		Test C	Test Conditions		Тур.	Max.	Unit
V _{(BR)CBO}	Collector - Base	I _C = 100μA	$I_{E} = 0$	300			V
	breakdown voltage						v
V _{(BR)CEO}	Collector - Emitter	I _C = 1mA	$I_B = 0^*$	300			V
	breakdown voltage						
V _{(BR)EBO}	Emitter - Base	I _E = 100μA	$I_{\rm C} = 0$	6			V
	breakdown voltage						v
I _{CBO}	Collector cut-off current	V _{CB} = 200V	$I_E = 0$			0.1	μA
I _{EBO}	Emitter cut-off current	$V_{EB} = 6V$	$I_{\rm C} = 0$			0.1	μA
V _{CE(sat)}	Collector - Emitter	I _C = 20mA	I _B = 2mA			0.5	V
	saturation voltage						
V _{BE(sat)}	Base - Emitter	I _C = 20mA	I _B = 2mA			0.9	V
	saturation voltage						
h _{FE}	DC Current gain	$I_{\rm C} = 1 {\rm mA}$	V _{CE} = 10V*	25			_
		I _C = 10mA	$V_{CE} = 10V^{*}$	40			
		I _C = 30mA	V _{CE} = 10V*	40			
f _T	Transition frequency	I _C = 10mA	$V_{CE} = 20V$	50			
		f = 20MHz		50			MHz
C _{ob}	Output capacitance	V _{CB} = 20V	$I_E = 0$			<u> </u>	
		f = 1MHz				6	pF

* Pulse test t_p = 200 μs , δ = 2%

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